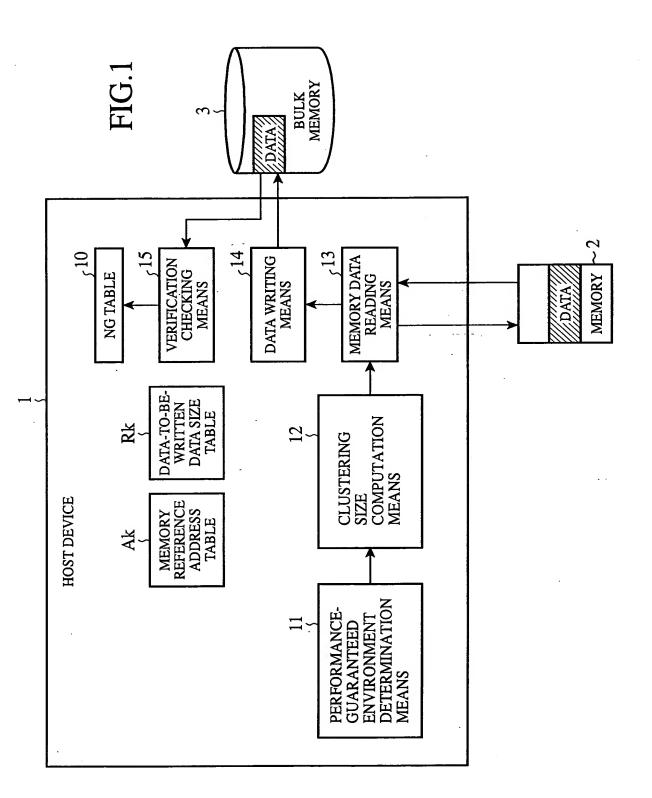
Tatsuya MITSUGI, et al.
INFORMATION STORAGE SYSTEM
June 20, 2005
Richard C. Turner
Q87978
1 of 9
10 /540159

1/9

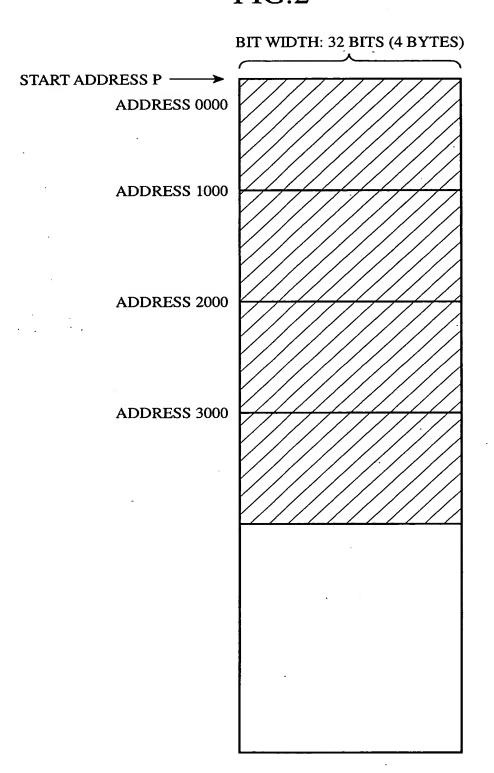


. .

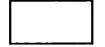
10/540159

2/9

FIG.2







202-293-7060 Q87978 10/540159 3 of 9

FIG.3

MEMORY REFERENCE ADDRESS TABLE

A0	A 1	A2	А3	A4
ADDRESS	ADDRESS	ADDRESS	ADDRESS	END
P	1000	2000	3000	MARK

FIG.4

DATA-TO-BE-WRITTEN SIZE TABLE

Ro	Ro R1		R3	R4	
4k	4k	4k	4k	0k	
BYTES	BYTES	BYTES	BYTES	BYTES	

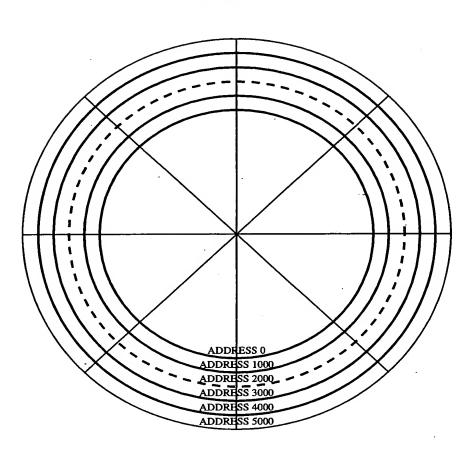
FIG.5

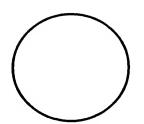
NG TABLE

ADDRESS	ADDRESS	ADDRESS	ADDRESS	ADDRESS	ADDRESS
0	1000	2000	3000	4000	5000
0	0	×	0	0	0

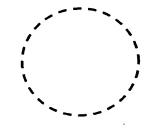
10/540159

FIG.6

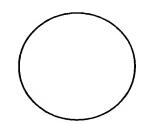




AREA INTO WHICH DATA ARE WRITTEN



AREA INTO WHICH DATA CANNOT BE WRITTEN



OTHER DATA AREA

FIG.7

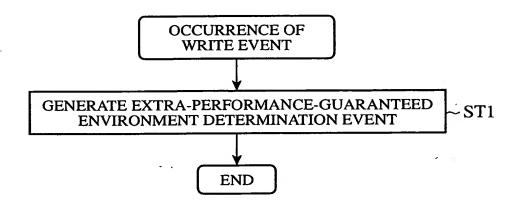
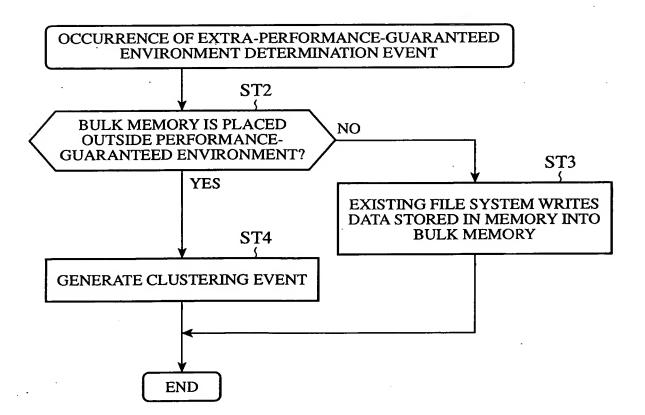


FIG.8



Carlot Commence of the Commence of the



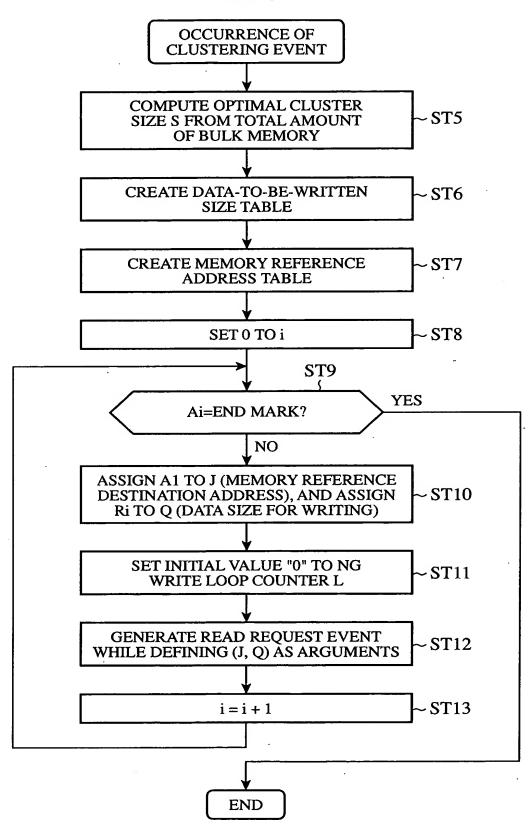


FIG.10

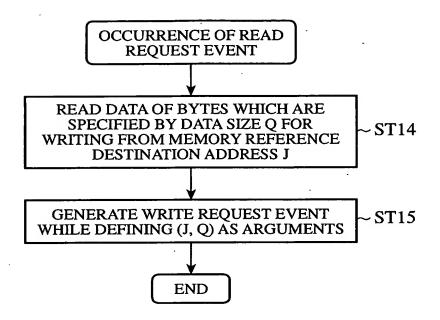


FIG.11

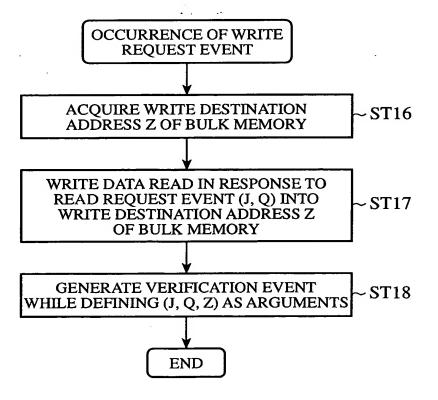
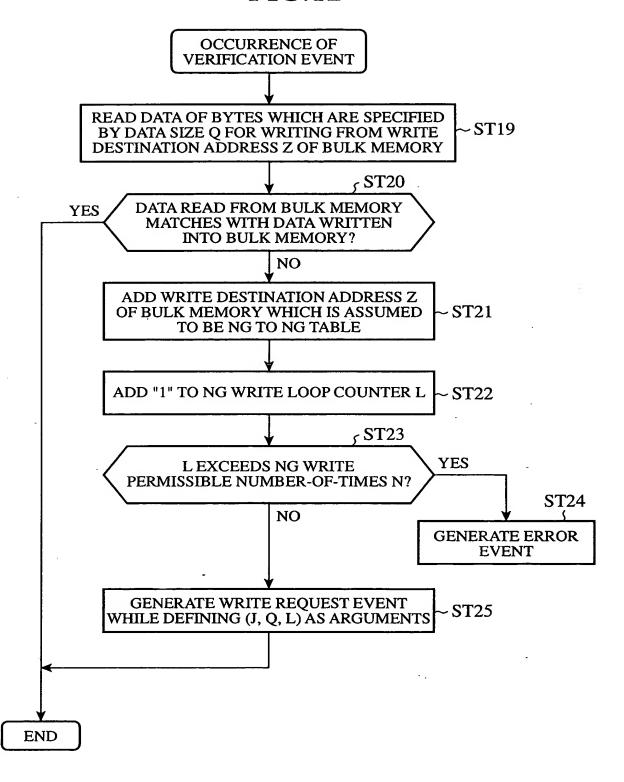


FIG.12



9/9

FIG.13

